

**sdmay23-28: Digital Chip Fabrication**

Week 4 Report

October 17 - October 23

**Team Members**Tyler Green — *Software tools for testing*Katherine Gisi — *Team Lead*William Zogg — *Documentation / Research*Fulai Zhu — *Documentation*Aaron Sledge — *Documentation & Software tool research***Summary of Progress this Report**

Found out about SNN torch which can be used to train our SNN

Decided on a leaky integrate and fire model for our SNN

**Pending Issues**

Still unsure about how to draw the model for our SNN

Not sure how we will make the idea novel in order to be accepted by efabless

**Plans for Upcoming Reporting Period**

Get advice for drawing SNN Diagram maybe walk through an example

**Individual Contributions**

Team Member	Contribution	Weekly Hours	Total Hours
Tyler Green	Researching snn tools to use for a model, found we can use snn torch to train a model for MNIST handwritten digits and use the determined weights to test our design	6	0
Katherine Gisi	Completed GPIO and LA tutorial.	4	0
William Zogg	Worked on documentation and researched GPIO / SPI interfaces for harness.	4	
Fulai Zhu	Help to finish the Design Document: Design Contextualization and Exploration.	4	
Aaron Sledge	studied code within each test bench provided by efabless to understand what it is they are testing for as well as helped finish design contextualization and exploration document.	6	

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**Gitlab Activity Summary**

Nothing to report.

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