

sdmay23-28: Digital Chip Fabrication

Week 7 Report

November 7 - November 27

Team MembersTyler Green — *Software tools for testing*Katherine Gisi — *Team Lead*William Zogg — *Documentation / SNN Design*Fulai Zhu — *Documentation*Aaron Sledge — *Software Tool Research & documentation***Summary of Progress this Report**

Got a fairly good start to the documentation our client wants

Got an idea of how much memory in SRAM is provided to us

Continued to refine neuron model

Pending Issues

Unsure if we are able to request more memory for SRAM on our device from efabless

Plans for Upcoming Reporting Period

Get a rough draft of our final presentation by next Friday

Practice running through presentation

Finish final design document

Individual Contributions

Team Member	Contribution	Weekly Hours	Total Hours
Tyler Green	Researching SRAM options, reading slack forum to find a solution and example code, reading on how much we have so we can determine a plan for implementing neurons	6	0
Katherine Gisi	Created first draft of design diagram. Worked on learning Verilog.	7	0
William Zogg	Worked on documentation and started exploring optimizations / software algorithms for our SNN.	5	
Fulai Zhu	Worked on documentation that our client	4	

	wants by the end of 2023		
Aaron Sledge	Continued to work on documentation that our client wants by the end of 2023	4	

Gitlab Activity Summary

Nothing to report.
